IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Masatoshi TAKAHASHI et al.

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For: SEMICONDUCTOR PROCESSING DEVICE AND IC CARD

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

Documents AE and AF on the attached List were cited in the specification, on pages 1 and 2, and their relevance is indicated therein.

Documents AA-AC and AG-AP were cited in the International Search Report in the underlying PCT Application.

Respectfully submitted,

MWS:sjk

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October 26, 2005

Mitchell W. Shapiro

Reg. No. 31,568

FORM PTO-1449					Atty. Docket No	•	Appln.	No.
INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT					XA-10261 10/5		10/52	1,553
					Applicant Masatoshi TAKAHASHI et al.			
					January 19, 2005			
								U.S. PAT
Examiner Initial		Document Number	Date	Name		Class	Sub- class	Filing Date
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	AB	6,097,059	08/01/00	Yamada		257	321	
	AC	5,553,016	09/03/96	Takebuchi		365	51	
	AD	5,768,194	06/16/98	Matsubara et al.		365	185.33	
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	AE	63-266698	02/11/88	Japan				Yes
	AF	5-266219	10/15/93	Japan				Abstract
	AG	10-198776	07/31/98	Japan				Abstract
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